

PICOSAUR

PREDATOR SDR + EXTENSIBLE X86 PROCESSING PLATFORM

PICOSAUR PROVIDES FLEXIBILITY FOR LOW-SWAP APPLICATIONS

A Predator SDR merged with up to 4 highly-capable x86 SFF computers allows for Picosaur to be utilized for countless applications.



SPECIFICATIONS

- 2x RX/TX channels, up to 200MHz IBW
- 10MHz and 1PPS timing signals
- 1 GbE RJ45 command and control
- 1/2U 12.25" x 4.5" x 4.13"
- 3/4U 19.0" x 4.5" x 4.13"

GLOBAL AMERICAN COMMELL® LP-1797 X86 SPECS:

- Intel® Tiger Lake UP3 Processor
- LP-1797 with i7-1185G7E
- 32GB LPDDR4
- 10GbE High-speed Interface

PREDATOR SDR SPECS:

- FPGA XCZU11EG-1FFVC1156E
 - 4-GB RAM FPGA processor (PS)
 - 4-GB RAM FPGA programmable logic (PL)
- ADRV9026 RF Transceiver
 - 75 MHz to 6 GHz RX and TX
 - 245.76-Msps maximum sample rate
 - Two independent tuners
- Up to 4x 10GbE High-speed Interface

PICOSAUR

ADDRESS

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